



500.39910X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: M. MIYAZAKI, et al.  
Application No.: 09/811,387  
Filed: March 20, 2001  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
IN WHICH OPERATING FREQUENCY, SUPPLY  
VOLTAGE AND SUBSTRATE BIAS VOLTAGE ARE  
CONTROLLABLE TO REDUCE POWER CONSUMPTION  
Art Unit: 2816  
Examiner: Kenneth B. Wells

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 8, 2004

Sir:

In response to the Office Action dated August 11, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims;**

**Remarks are included following the amendments.**

RECEIVED  
JAN 15 2004  
TECHNOLOGY CENTER 2800